In re application of:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

§

Docket No.: 24061.461

(2002-0066)

Serial No.: 10/600,393 Group Art Unit: 2822

88888 Filed: 06/20/2003 Examiner: Thomas Toniae M.

For: METHOD OF FORMING DUAL Conf. No.: 8529

GATE INSULATOR LAYERS FOR **CMOS APPLICATIONS** 

## **TRANSMITTAL**

Mail Stop: AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

0

Chi-Chun Chen et al.

Dear Sir:

Enclosed for filing in the above-identified patent application.

- 1. Amendment in Response to the Office Action dated September 22, 2004;
- 2. Revocation/New Power of Attorney By Assignee of Entire Interest Including Certification for Taking Action By Assignee Under 37 CFR 3.73(b);
- 3. Information Disclosure Statement and USPTO Form 1449;
- 4. Checks in the total amount of \$380.00 (\$200.00 additional independent claim; \$180 IDS);
- 5. This transmittal (in duplicate); and
- Return postcard.

The Director is authorized to charge any deficiency fees or credit any overpayments to Deposit Account No. 08-1394 of Haynes and Boone, LLP.

Respectfully submitted

David M. O'Dell

Registration No. 42,044

Date: 12-22-04

HAYNES AND BOONE, LLP 901 Main Street, Suite 3100 Dallas, Texas 75202-3789 Telephone: 972-739-8630

Facsimile: 214-200-0853

File: 24061.461

**CERTIFICATE OF MAILING** 

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below:

R-94494 1.DOC



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	1	§	Docket No.: 24061.461
Chi-Chun Chen et a	al.		(2002-0066)
		§	
Serial No.: 10/600,393		§	Group Art Unit: 2822
		§	
Filed: 06/20/2003		§	Examiner: Thomas Toniae M.
••		§	
For: METHOD OF FOR	UMING DUAL	§	Conf. No.: 8529

Commissioner for Patents Mail Stop Amendment P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT**

Sir:

In response to the Office action of September 22, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.

GATE INSULATOR LAYERS FOR

**CMOS APPLICATIONS** 

12/30/2004 HVUONG1 00000023 10600393

02 FC:1201

200.00 OP